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APPLICANTS

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**** CONTINUING DATA *******

This application is a 371 of PCT/EP04/50909 05/25/2004

**** FOREIGN APPLICATIONS *******

EUROPEAN PATENT OFFICE (EPO) 03013632.9 06/16/2003

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****

12/04/2006

Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and Acknowledged	(GUNNAR J GISSEL/ Examiner's Signature)		Initials	GERMANY	2	15	4

ADDRESS

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TITLE

Method for manufacturing an electronic arrangement and an electronic circuit arrangement

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